

REMARKS/ARGUMENTS

1. Rejection of claims 1, 2, and 4-7 under 35 U.S.C. 103(a):

Claims 1, 2, and 4-7 are rejected under 35 U.S.C. 103(a) as being unpatentable over Applicant's Admitted Prior Art (AAPA) in view of Hendrickson (US 2002/0090045).

Response:

Claim 1 has been amended to overcome this rejection. Claim 1 now recites that the primary phase selector outputs two consecutive discrete clocks and at least one interpolated clock with a phase between the phases of the two consecutive discrete clocks, according to the select signal. Furthermore, claim 1 specifies that the two consecutive discrete clocks and the interpolated clock have approximately the same frequency as the reference clock and the input data. Support for this amendment is found at the end of paragraph [0006], which states that the frequency of the reference clock CLK_{ref} is approximately the same as that of the output data $DATA_{out}$. In addition, support is found in paragraph [0021], which states that one of two consecutive discrete clocks or an interpolated clock is found to be more synchronized with the input data $DATA_{in}$.

On the other hand, the AAPA does not teach that the primary phase selector outputs two consecutive discrete clocks and at least one interpolated clock with a phase between the phases of the two consecutive discrete clocks, as noted by the Examiner on page 3 of the Office action dated June 28, 2007. Furthermore, Hendrickson teaches in paragraphs [0024] and [0025] that the external clock signal 39a is multiplied by a clock multiplier 39 to create a clock signal having a greater frequency than the reference clock signal. Delayed versions of the multiplied clock are then interpolated for creating versions of the multiplied clock with finer steps. However, the

multiplied clock and interpolated versions of the multiplied clock do not have approximately the same frequency as both the reference clock and the input data, as is recited in the currently amended claim 1.

5 For these reasons, the applicant submits that the currently amended claim 1 is patentable over the cited prior art, and should be allowed. Claims 2 and 4-7 are dependent on claim 1, and should be allowed if claim 1 is allowed. Reconsideration of claims 1, 2, and 4-7 is therefore respectfully requested.

10 2. Rejection of claim 3 under 35 U.S.C. 103(a):

 Claim 3 is rejected under 35 U.S.C. 103(a) as being unpatentable over Applicant's Admitted Prior Art (AAPA) in view of Hendrickson (US 2002/0090045), further in view of Lee et al. (US 2002/0085656).

15 **Response:**

 Claim 3 is dependent on claim 1, and should be allowed if claim 1 is allowed. Reconsideration of claim 3 is therefore respectfully requested.

3. Rejection of claims 8 and 9 under 35 U.S.C. 103(a):

20 Claims 8 and 9 are rejected under 35 U.S.C. 103(a) as being unpatentable over Applicant's Admitted Prior Art (AAPA) in view of Hendrickson (US 2002/0090045), further in view of Chen (US 5,850,422).

Response:

25 Claims 8 and 9 are dependent on claim 1, and should be allowed if claim 1 is allowed. Reconsideration of claims 8 and 9 is therefore respectfully requested.

4. Rejection of claim 10 under 35 U.S.C. 103(a):

Claim 10 is rejected under 35 U.S.C. 103(a) as being unpatentable over Applicant's Admitted Prior Art (AAPA) in view of Hendrickson (US 2002/0090045), further in view of Cranford, Jr. et al. (US 2004/0170244).

5 **Response:**

Claim 10 is dependent on claim 1, and should be allowed if claim 1 is allowed.
Reconsideration of claim 10 is therefore respectfully requested.

10 In view of the claim amendments and the above arguments in favor of patentability,
the applicant respectfully requests that a timely Notice of Allowance be issued in this
case.

Sincerely yours,

15 Winston Hsu

Date: 08.08.2007

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is 12 hours behind the Taiwan time, i.e. 9 AM in D.C. = 9 PM in Taiwan.)

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